INT32-C. Ensure that operations on signed integers do not result in overflow

Signed integer overflow is *undefined behavior* 36. Consequently, *implementations* have considerable latitude in how they deal with signed integer overflow. (See MSC15-C. Do not depend on undefined behavior.) An implementation that defines signed integer types as being modulo, for example, need not detect integer overflow. Implementations may also trap on signed arithmetic overflows, or simply assume that overflows will never happen and generate object code accordingly. It is also possible for the same conforming implementation to emit code that exhibits different behavior in different contexts. For example, an implementation may determine that a signed integer loop control variable declared in a local scope cannot overflow and may emit efficient code on the basis of that determination, while the same implementation may determine that a global variable used in a similar context will wrap.

For these reasons, it is important to ensure that operations on signed integers do not result in overflow. Of particular importance are operations on signed integer values that originate from a *tainted source* and are used as

- Integer operands of any pointer arithmetic, including array indexing
- The assignment expression for the declaration of a variable length array
- The postfix expression preceding square brackets [], or the expression in square brackets [], of a subscripted designation of an element of an array object
- Function arguments of type size_t or rsize_t (for example, an argument to a memory allocation function)

Integer operations will overflow if the resulting value cannot be represented by the underlying representation of the integer. The following table indicates which operations can result in overflow.

<table>
<thead>
<tr>
<th>Operator</th>
<th>Overflow</th>
<th>Operator</th>
<th>Overflow</th>
<th>Operator</th>
<th>Overflow</th>
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<td>Yes</td>
<td>&gt;&gt;</td>
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<td>No</td>
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<td>No</td>
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<tr>
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<td>Yes</td>
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<td>Yes</td>
<td>^</td>
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<td>!</td>
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<td>No</td>
<td>unary -</td>
<td>Yes</td>
<td>?:</td>
<td>No</td>
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</table>

The following sections examine specific operations that are susceptible to integer overflow. When operating on integer types with less precision than int, integer promotions are applied. The usual arithmetic conversions may also be applied to (implicitly) convert operands to equivalent types before arithmetic operations are performed. Programmers should understand integer conversion rules before trying to implement secure arithmetic operations. (See INT02-C. Understand integer conversion rules.)

Implementation Details

GNU GCC invoked with the -fwrapv command-line option defines the same modulo arithmetic for both unsigned and signed integers.

GNU GCC invoked with the -ftrapv command-line option causes a trap to be generated when a signed integer overflows, which will most likely abnormally exit. On a UNIX system, the result of such an event may be a signal sent to the process.

GNU GCC invoked without either the -fwrapv or the -ftrapv option may simply assume that signed integers never overflow and may generate object code accordingly.

Atomic Integers

The C Standard defines the behavior of arithmetic on atomic signed integer types to use two's complement representation with silent wraparound on overflow; there are no undefined results. Although defined, these results may be unexpected and therefore carry similar risks to *unsigned integer wrapping*. (See INT30-C. Ensure that unsigned integer operations do not wrap.) Consequently, signed integer overflow of atomic integer types should also be prevented or detected.

Addition

Addition is between two operands of arithmetic type or between a pointer to an object type and an integer type. This rule applies only to addition between two operands of arithmetic type. (See ARR37-C. Do not add or subtract an integer to a pointer to a non-array object and ARR30-C. Do not form or use out-of-bounds pointers or array subscripts.)

Incrementing is equivalent to adding 1.
Noncompliant Code Example
This noncompliant code example can result in a signed integer overflow during the addition of the signed operands \texttt{si\_a} and \texttt{si\_b}:

```c
void func(signed int si\_a, signed int si\_b) {
    signed int sum = si\_a + si\_b;
    /* ... */
}
```

Compliant Solution
This compliant solution ensures that the addition operation cannot overflow, regardless of representation:

```c
#include <limits.h>
void f(signed int si\_a, signed int si\_b) {
    signed int sum;
    if (((si\_b > 0) && (si\_a > (INT\_MAX - si\_b))) ||
        ((si\_b < 0) && (si\_a < (INT\_MIN - si\_b))) ) { /* Handle error */
        // Code to handle error
    } else {
        sum = si\_a + si\_b;
    }
    /* ... */
}
```

Compliant Solution (GNU)
This compliant solution uses the GNU extension \texttt{__builtin_sadd\_overflow}, available with GCC, Clang, and ICC:

```c
void f(signed int si\_a, signed int si\_b) {
    signed int sum;
    if (__builtin_sadd_overflow(si\_a, si\_b, &sum)) { /* Handle error */
        // Code to handle error
    }
    /* ... */
}
```

Subtraction
Subtraction is between two operands of arithmetic type, two pointers to qualified or unqualified versions of compatible object types, or a pointer to an object type and an integer type. This rule applies only to subtraction between two operands of arithmetic type. (See ARR36-C. Do not subtract or compare two pointers that do not refer to the same array, ARR37-C. Do not add or subtract an integer to a pointer to a non-array object, and ARR30-C. Do not form or use out-of-bounds pointers or array subscripts for information about pointer subtraction.)

Decrementing is equivalent to subtracting 1.

Noncompliant Code Example
This noncompliant code example can result in a signed integer overflow during the subtraction of the signed operands \texttt{si\_a} and \texttt{si\_b}:

```c
void func(signed int si\_a, signed int si\_b) {
    signed int diff = si\_a - si\_b;
    /* ... */
}
```

Compliant Solution
This compliant solution tests the operands of the subtraction to guarantee there is no possibility of signed overflow, regardless of representation:
#include <limits.h>

void func(signed int si_a, signed int si_b) {
    signed int diff;
    if ((si_b > 0 && si_a < INT_MIN + si_b) ||
        (si_b < 0 && si_a > INT_MAX + si_b)) {
        /* Handle error */
    } else {
        diff = si_a - si_b;
    }
    /* ... */
}

**Compliant Solution (GNU)**

This compliant solution uses the GNU extension `__builtin_ssub_overflow`, available with GCC, Clang, and ICC:

```c
void func(signed int si_a, signed int si_b) {
    signed int diff;
    if (__builtin_ssub_overflow(si_a, si_b, &diff)) {
        /* Handle error */
    } else {
        /* ... */
    }
}
```

**Multiplication**

Multiplication is between two operands of arithmetic type.

**Noncompliant Code Example**

This noncompliant code example can result in a signed integer overflow during the multiplication of the signed operands `si_a` and `si_b`:

```c
void func(signed int si_a, signed int si_b) {
    signed int result = si_a * si_b;
    /* ... */
}
```

**Compliant Solution**

The product of two operands can always be represented using twice the number of bits than exist in the precision of the larger of the two operands. This compliant solution eliminates signed overflow on systems where `long long` is at least twice the precision of `int`:
#include <stddef.h>
#include <assert.h>
#include <limits.h>
#include <inttypes.h>

extern size_t popcount(uintmax_t);
#define PRECISION(umax_value) popcount(umax_value)

void func(signed int si_a, signed int si_b) {
    signed int result;
    signed long long tmp;
    assert(PRECISION(ULLONG_MAX) >= 2 * PRECISION(UINT_MAX));
    tmp = (signed long long)si_a * (signed long long)si_b;

    /*
    * If the product cannot be represented as a 32-bit integer,
    * handle as an error condition.
    */
    if ((tmp > INT_MAX) || (tmp < INT_MIN)) {
        /* Handle error */
        } else {
            result = (int)tmp;
        /* ... */
    }
}

The assertion fails if long long has less than twice the precision of int. The PRECISION() macro and pop count() function provide the correct precision for any integer type. (See INT35-C. Use correct integer precisions.)

Compliant Solution

The following portable compliant solution can be used with any conforming implementation, including those that do not have an integer type that is at least twice the precision of int:

#include <limits.h>

void func(signed int si_a, signed int si_b) {
    signed int result;
    if (si_a > 0) { /* si_a is positive */
        if (si_b > 0) { /* si_a and si_b are positive */
            if (si_a > (INT_MAX / si_b)) {
                /* Handle error */
            } else { /* si_a positive, si_b nonpositive */
                if (si_b < (INT_MIN / si_a)) {
                    /* Handle error */
                } /* si_a positive, si_b nonpositive */
            } /* si_a is nonpositive */
    } /* si_a is positive */
    else { /* si_a is nonpositive */
        if (si_b > 0) { /* si_a is nonpositive, si_b is positive */
            if (si_a < (INT_MIN / si_b)) {
                /* Handle error */
            } /* si_a is nonpositive */
        } /* si_a and si_b are nonpositive */
        else { /* si_a and si_b are nonpositive */
            if (si_a != 0) && (si_b < (INT_MAX / si_a)) {
                /* Handle error */
            } /* End if si_a and si_b are nonpositive */
        } /* End if si_a is nonpositive */
    }
    result = si_a * si_b;
}
Compliant Solution (GNU)

This compliant solution uses the GNU extension __builtin_smul_overflow, available with GCC, Clang, and ICC:

```c
void func(signed int si_a, signed int si_b) {
    signed int result;
    if (__builtin_smul_overflow(si_a, si_b, &result)) {
        /* Handle error */
    }
}
```

Division

Division is between two operands of arithmetic type. Overflow can occur during two's complement signed integer division when the dividend is equal to the minimum (negative) value for the signed integer type and the divisor is equal to 1. Division operations are also susceptible to divide-by-zero errors. (See INT33-C. Ensure that division and remainder operations do not result in divide-by-zero errors.)

Noncompliant Code Example

This noncompliant code example prevents divide-by-zero errors in compliance with INT33-C. Ensure that division and remainder operations do not result in divide-by-zero errors but does not prevent a signed integer overflow error in two's-complement.

```c
void func(signed long s_a, signed long s_b) {
    signed long result;
    if (s_b == 0) {
        /* Handle error */
    } else {
        result = s_a / s_b;
    }
    /* ... */
}
```

Implementation Details

On the x86-32 architecture, overflow results in a fault, which can be exploited as a denial-of-service attack.

Compliant Solution

This compliant solution eliminates the possibility of divide-by-zero errors or signed overflow:

```c
#include <limits.h>

void func(signed long s_a, signed long s_b) {
    signed long result;
    if ((s_b == 0) || ((s_a == LONG_MIN) && (s_b == -1))) {
        /* Handle error */
    } else {
        result = s_a / s_b;
    }
    /* ... */
}
```

Remainder

The remainder operator provides the remainder when two operands of integer type are divided. Because many platforms implement remainder and division in the same instruction, the remainder operator is also susceptible to arithmetic overflow and division by zero. (See INT33-C. Ensure that division and remainder operations do not result in divide-by-zero errors.)

Noncompliant Code Example
Many hardware architectures implement remainder as part of the division operator, which can overflow. Overflow can occur during a remainder operation when the dividend is equal to the minimum (negative) value for the signed integer type and the divisor is equal to 1. It occurs even though the result of such a remainder operation is mathematically 0. This noncompliant code example prevents divide-by-zero errors in compliance with INT33-C. Ensure that division and remainder operations do not result in divide-by-zero errors but does not prevent integer overflow:

```c
void func(signed long s_a, signed long s_b) {
    signed long result;
    if (s_b == 0) {
        /* Handle error */
    } else {
        result = s_a % s_b;
    }
    /* ... */
}
```

**Implementation Details**

On x86-32 platforms, the remainder operator for signed integers is implemented by the `idiv` instruction code, along with the divide operator. Because \( \text{LONG_MIN} / 1 \) overflows, it results in a software exception with \( \text{LONG_MIN} \% 1 \) as well.

**Compliant Solution**

This compliant solution also tests the remainder operands to guarantee there is no possibility of an overflow:

```c
#include <limits.h>
void func(signed long s_a, signed long s_b) {
    signed long result;
    if ((s_b == 0) || ((s_a == LONG_MIN) && (s_b == -1))) {
        /* Handle error */
    } else {
        result = s_a % s_b;
    }
    /* ... */
}
```

**Left-Shift Operator**

The left-shift operator takes two integer operands. The result of \( E_1 \ll E_2 \) is \( E_1 \) left-shifted \( E_2 \) bit positions; vacated bits are filled with zeros.

The C Standard, 6.5.7, paragraph 4 [ISO/IEC 9899:2011], states

> If \( E_1 \) has a signed type and nonnegative value, and \( E_1 \times 2^{E_2} \) is representable in the result type, then that is the resulting value; otherwise, the behavior is undefined.

In almost every case, an attempt to shift by a negative number of bits or by more bits than exist in the operand indicates a logic error. These issues are covered by INT34-C. Do not shift an expression by a negative number of bits or by greater than or equal to the number of bits that exist in the operand.

**Noncompliant Code Example**

This noncompliant code example performs a left shift, after verifying that the number being shifted is not negative, and the number of bits to shift is valid. The PRECISION() macro and popcount() function provide the correct precision for any integer type. (See INT35-C. Use correct integer precisions.) However, because this code does no overflow check, it can result in an unrepresentable value.
#include <limits.h>
#include <stddef.h>
#include <inttypes.h>
extern size_t popcount(uintmax_t);
#define PRECISION(umax_value) popcount(umax_value)

void func(signed long si_a, signed long si_b) {
    signed long result;
    if ((si_a < 0) || (si_b < 0) ||
        (si_b >= PRECISION(ULONG_MAX)) ||
        (si_a > (LONG_MAX >> si_b))) {
        /* Handle error */
    } else {
        result = si_a << si_b;
    }
    /* ... */
}

Compliant Solution
This compliant solution eliminates the possibility of overflow resulting from a left-shift operation:

```c
#include <limits.h>
#include <stddef.h>
#include <inttypes.h>
extern size_t popcount(uintmax_t);
#define PRECISION(umax_value) popcount(umax_value)

void func(signed long si_a, signed long si_b) {
    signed long result;
    if ((si_a < 0) || (si_b < 0) ||
        (si_b >= PRECISION(ULONG_MAX)) ||
        (si_a > (LONG_MAX >> si_b))) {
        /* Handle error */
    } else {
        result = si_a << si_b;
    }
    /* ... */
}
```

Unary Negation
The unary negation operator takes an operand of arithmetic type. Overflow can occur during two's complement unary negation when the operand is equal to the minimum (negative) value for the signed integer type.

Noncompliant Code Example
This noncompliant code example can result in a signed integer overflow during the unary negation of the signed operand `s_a`:

```c
void func(signed long s_a) {
    signed long result = -s_a;
    /* ... */
}
```

Compliant Solution
This compliant solution tests the negation operation to guarantee there is no possibility of signed overflow:
#include <limits.h>

void func(signed long s_a) {
    signed long result;
    if (s_a == LONG_MIN) {
        /* Handle error */
    } else {
        result = -s_a;
    }
    /* ... */
}

## Risk Assessment

Integer overflow can lead to buffer overflows and the execution of arbitrary code by an attacker.

<table>
<thead>
<tr>
<th>Rule</th>
<th>Severity</th>
<th>Likelihood</th>
<th>Remediation Cost</th>
<th>Priority</th>
<th>Level</th>
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</thead>
<tbody>
<tr>
<td>INT32-C</td>
<td>High</td>
<td>Likely</td>
<td>High</td>
<td>P9</td>
<td>L2</td>
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### Automated Detection

<table>
<thead>
<tr>
<th>Tool</th>
<th>Version</th>
<th>Checker</th>
<th>Description</th>
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<td>integer-overflow</td>
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<td>ALLOC.SIZE.IOFLOW</td>
<td>Integer overflow of allocation size</td>
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<td>ALLOC.SIZE.MULOFLOW</td>
<td>Multiplication overflow of allocation size</td>
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<td>ALLOC.SIZE.SUBUFLOW</td>
<td>Subtraction underflow of allocation size</td>
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<td>MISC.MEM.SIZE.ADDOFLOW</td>
<td>Addition overflow of size</td>
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<td>MISC.MEM.SIZE.BAD</td>
<td>Unreasonable size argument</td>
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<td>Integer overflow or underflow in constant expression in '+', '-', '*' operator</td>
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<td>CERT C: Rule INT32-C</td>
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<td>TrustInSoft Analyzer</td>
<td>1.38</td>
<td>signed_overflow</td>
<td>Exhaustively verified (see one compliant and one non-compliant example).</td>
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Related Vulnerabilities
Search for vulnerabilities resulting from the violation of this rule on the CERT website.

Related Guidelines
Key here (explains table format and definitions)

<table>
<thead>
<tr>
<th>Taxonomy</th>
<th>Taxonomy item</th>
<th>Relationship</th>
</tr>
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<tbody>
<tr>
<td>CERT C</td>
<td>INT02-C. Understand integer conversion rules</td>
<td>Prior to 2018-01-12: CERT: Unspecified Relationship</td>
</tr>
<tr>
<td>CERT C</td>
<td>INT35-C. Use correct integer precisions</td>
<td>Prior to 2018-01-12: CERT: Unspecified Relationship</td>
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<tr>
<td>CERT C</td>
<td>INT33-C. Ensure that division and remainder operations do not result in divide-by-zero errors</td>
<td>Prior to 2018-01-12: CERT: Unspecified Relationship</td>
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<td>INT34-C. Do not shift an expression by a negative number of bits or by greater than or equal to the number of bits that exist in the operand</td>
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<td>MSC15-C. Do not depend on undefined behavior</td>
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<td>INT00-J. Perform explicit range checking to avoid integer overflow</td>
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<td>CWE 2.11</td>
<td>CWE-190, Integer Overflow or Wraparound</td>
<td>2017-05-18: CERT: Partial overlap</td>
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<td>CWE 2.11</td>
<td>CWE-191</td>
<td>2017-05-18: CERT: Partial overlap</td>
</tr>
<tr>
<td>CWE 2.11</td>
<td>CWE-680</td>
<td>2017-05-18: CERT: Partial overlap</td>
</tr>
</tbody>
</table>

CERT-CWE Mapping Notes
Key here for mapping notes

CWE-20 and INT32-C
See CWE-20 and ERR34-C

CWE-680 and INT32-C
Intersection( INT32-C, MEM35-C) = Ø
Intersection( CWE-680, INT32-C) =
  • Signed integer overflows that lead to buffer overflows
CWE-680 - INT32-C =
  • Unsigned integer overflows that lead to buffer overflows
INT32-C – CWE-680 =
  • Signed integer overflows that do not lead to buffer overflows

CWE-191 and INT32-C

Union( CWE-190, CWE-191) = Union( INT30-C, INT32-C)
Intersection( INT30-C, INT32-C) == Ø
Intersection( CWE-191, INT32-C) =
  • Underflow of signed integer operation
CWE-191 – INT32-C =
  • Underflow of unsigned integer operation
INT32-C – CWE-191 =
  • Overflow of signed integer operation

CWE-190 and INT32-C

Union( CWE-190, CWE-191) = Union( INT30-C, INT32-C)
Intersection( INT30-C, INT32-C) == Ø
Intersection( CWE-190, INT32-C) =
  • Overflow (wraparound) of signed integer operation
CWE-190 – INT32-C =
  • Overflow of unsigned integer operation
INT32-C – CWE-190 =
  • Underflow of signed integer operation

Bibliography

[Seacord 2013b] Chapter 5, "Integer Security"
[Viega 2005] Section 5.2.7, "Integer Overflow"